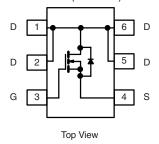
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# N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a, e</sup> Q <sub>g</sub> (Typ					
30	0.028 at V <sub>GS</sub> = 10 V	6	4.2 nC				
30	0.033 at $V_{GS}$ = 4.5 V	6	4.2 110				





#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Low On-Resistance
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

## APPLICATIONS

• DC/DC Converters, High Speed Switching

ABSOLUTE MAXIMUM RATIN	<b>IGS</b> (T <sub>A</sub> = 25 °C	, unless oth	erwise noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		6 <sup>e</sup>		
Continuous Drain Current $(T = 150 ^{\circ}\text{C})$	T <sub>C</sub> = 70 °C		6 <sup>e</sup>		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C	I <sub>D</sub>	5.5 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		4.4 <sup>b, c</sup>	A	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	25		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		2.1		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.1 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Movimum Power Dissipation	T <sub>C</sub> = 70 °C		1.6	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.3 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	T <sub>J</sub> , T <sub>stg</sub> - 55 to 150		
Soldering Recommendations (Peak Temperature)			260		

THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	75	100	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	50	0/1			

Notes:

a. Based on  $T_C = 25$  °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166  $^{\circ}\text{C/W}.$ 

e. Package limited.

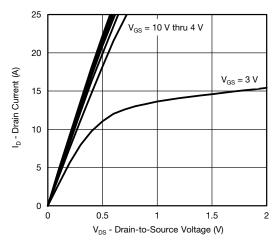
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	-				•	•
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		30		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250 \mu\text{A}$		- 4.8		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.2		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
	I <sub>DSS</sub> -	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 V$ , $V_{GS} = 10 V$	20			Α
	5	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5.5 \text{ A}$		0.023	0.028	Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		0.027	0.033	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5.5 \text{ A}$		24		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			424		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		100		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			42		
Total Gate Charge	Q <sub>g</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 5.5 A		8.2	13	nC
				4.2	7	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 5.5 A		1.4		
Gate-Drain Charge	Q <sub>gd</sub>			1.4		
Gate Resistance	Rg	f = 1 MHz	2.5	12.6	25.2	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			6	12	- ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3.4 $\Omega$		20	30	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4.4$ A, $V_{GEN} = 4.5$ V, $R_g = 1$ $\Omega$		14	21	
Fall Time	t <sub>f</sub>			10	20	
Turn-On Delay Time	t <sub>d(on)</sub>			3	6	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3.4 $\Omega$		11	20	
Turn-Off Delay Time	t <sub>d(off)</sub>	${\sf I}_{\sf D}\cong$ 4.4 A, ${\sf V}_{\sf GEN}$ = 10 V, ${\sf R}_{\sf g}$ = 1 $\Omega$		20	30	
Fall Time	t <sub>f</sub>			7	14	
Drain-Source Body Diode Characteristi	cs					
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	$T_{C} = 25 \ ^{\circ}C$			2.1	^
Pulse Diode Forward Current	I <sub>SM</sub>				25	A
Body Diode Voltage	V <sub>SD</sub>	$I_{S} = 4.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.82	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			13	20	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 4.4 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		6	12	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$F = 4.4 \text{ A}, \text{ ut/ut} = 100 \text{ A/}\mu\text{s}, \text{ I}_{\text{J}} = 25 ^{\circ}\text{C}$		8		
Reverse Recovery Rise Time	t <sub>b</sub>			5		ns

Notes:

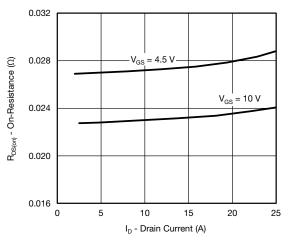
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

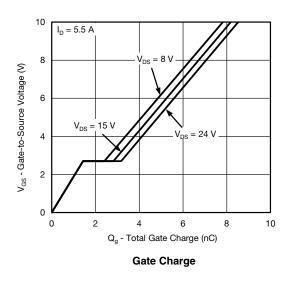
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

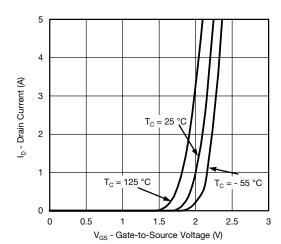




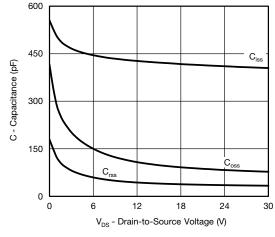


On-Resistance vs. Drain Current and Gate Voltage

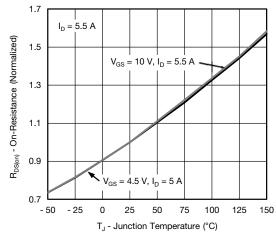




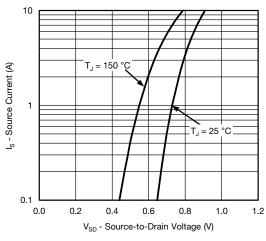
**Transfer Characteristics** 

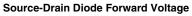


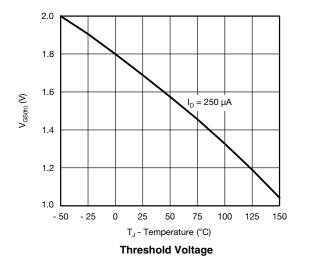


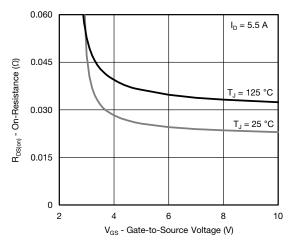


**On-Resistance vs. Junction Temperature** 

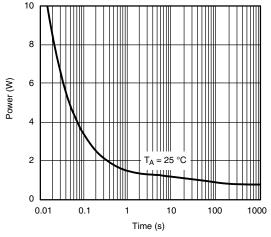




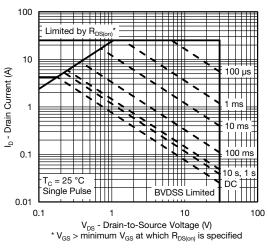




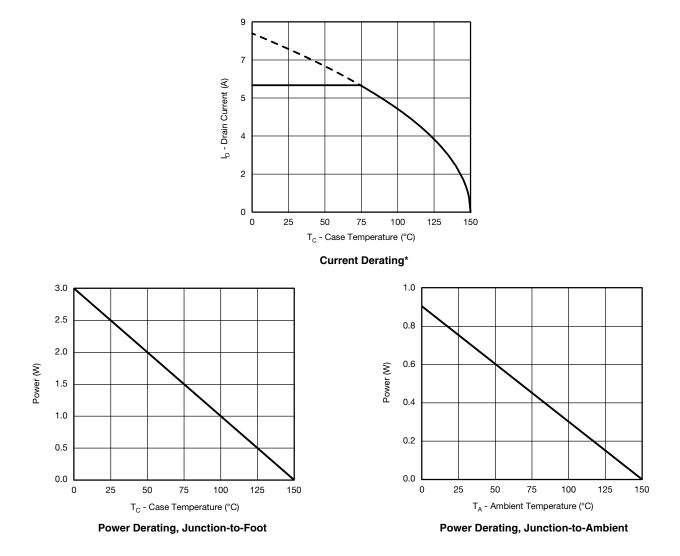
On-Resistance vs. Gate-to-Source Voltage



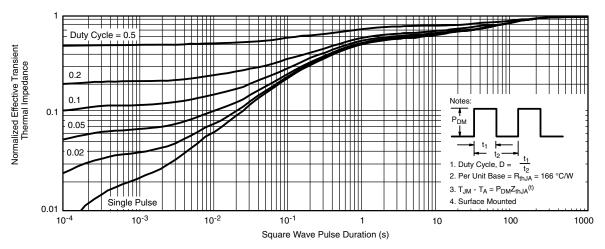
Single Pulse Power (Junction-to-Ambient)



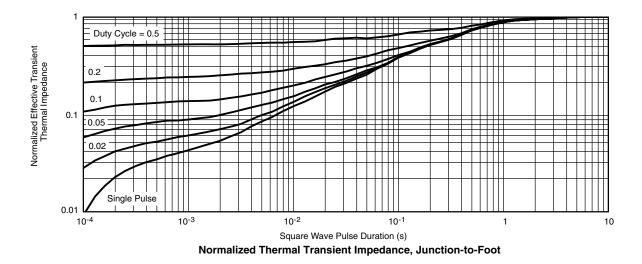
Safe Operating Area, Junction-to-Ambient



\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



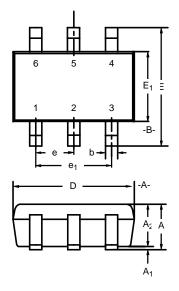
Normalized Thermal Transient Impedance, Junction-to-Ambient

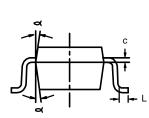


# **Din-Tek**

# Package Information Á

# SC-70: 6-LEADS

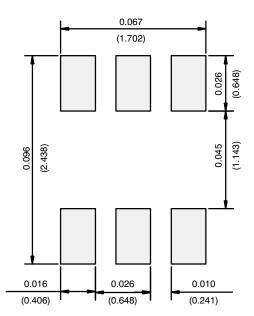




	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A <sub>1</sub>	-	-	0.10	-	-	0.004
A <sub>2</sub>	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC			0.026BSC		
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
٩	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						



### **RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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